QIPE JC85 AUG 13 2004 PATENT & TRADEMARK OFFICE

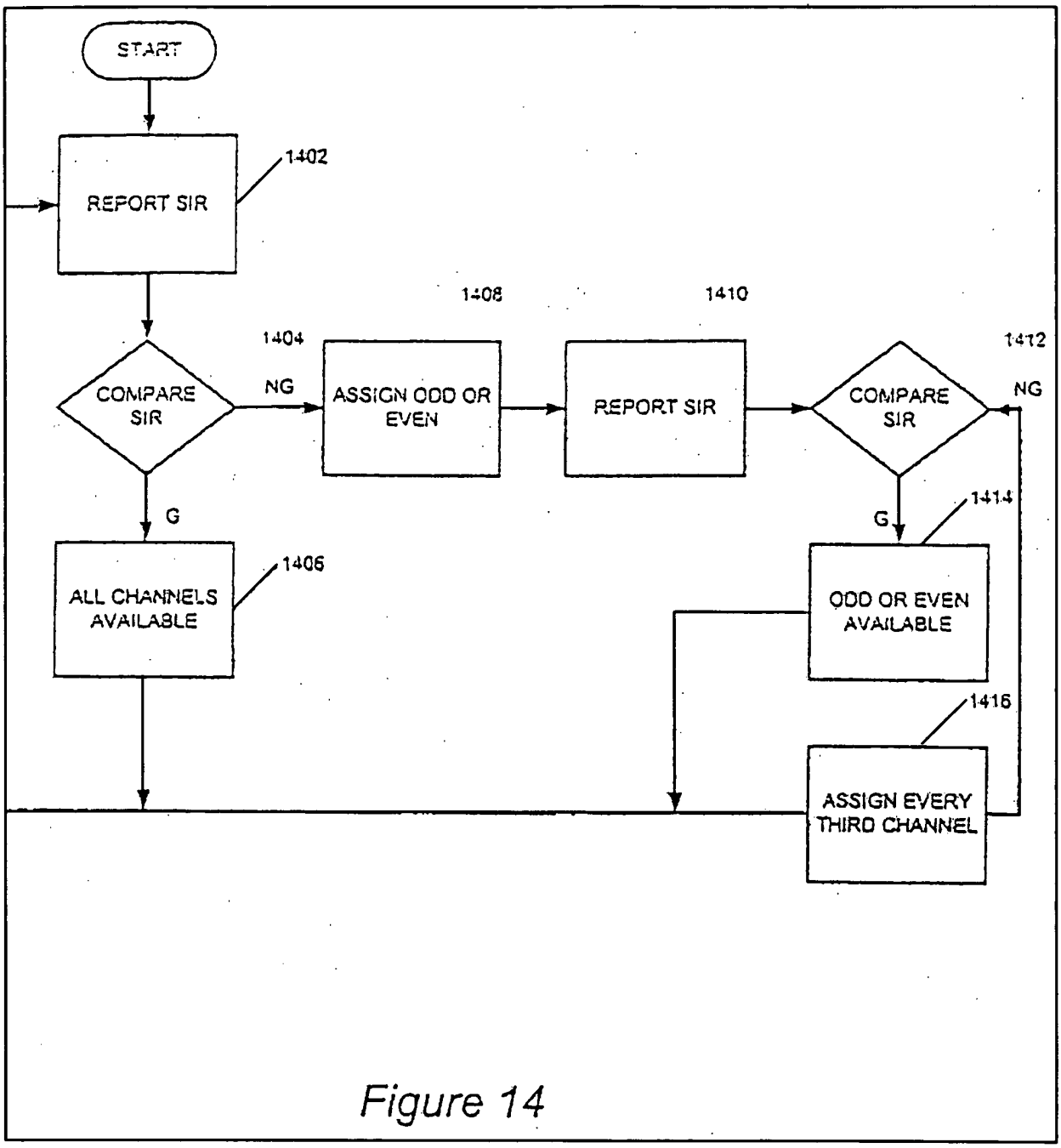

Figure 14

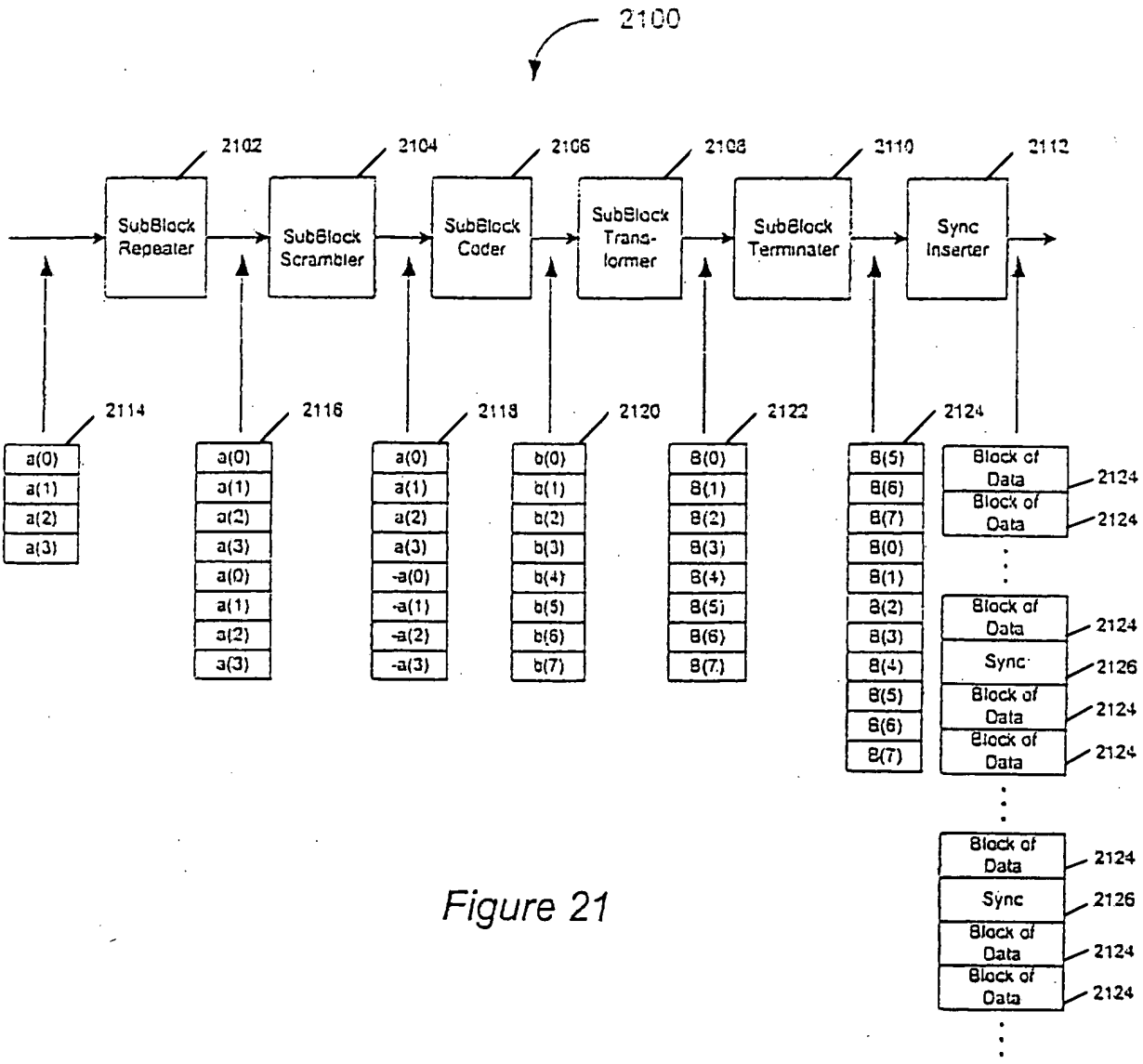
2100
2102
SubBlock Repeater
2104
SubBlock Scrambler
2106
SubBlock Coder
2108
SubBlock Trans-lormer
2110
SubBlock Terminater
2112
Sync Inserter
2114
a(0)
a(1)
a(2)
a(3)
2116
a(0)
a(1)
a(2)
a(3)
a(0)
a(1)
a(2)
a(3)
2118
a(0)
a(1)
a(2)
a(3)
-a(0)
-a(1)
-a(2)
-a(3)
2120
b(0)
b(1)
b(2)
b(3)
b(4)
b(5)
b(6)
b(7)
2122
B(0)
B(1)
B(2)
B(3)
B(4)
B(5)
B(6)
B(7)
2124
B(5)
B(6)
B(7)
B(0)
B(1)
B(2)
B(3)
B(4)
B(5)
B(6)
B(7)
Block of Data
2124
Block of Data
2124
Block of Data
2124
Sync
2126
Block of Data
2124
Block of Data
2124
Block of Data
2124
Sync
2126
Block of Data
2124
Block of Data
2124

Figure 21

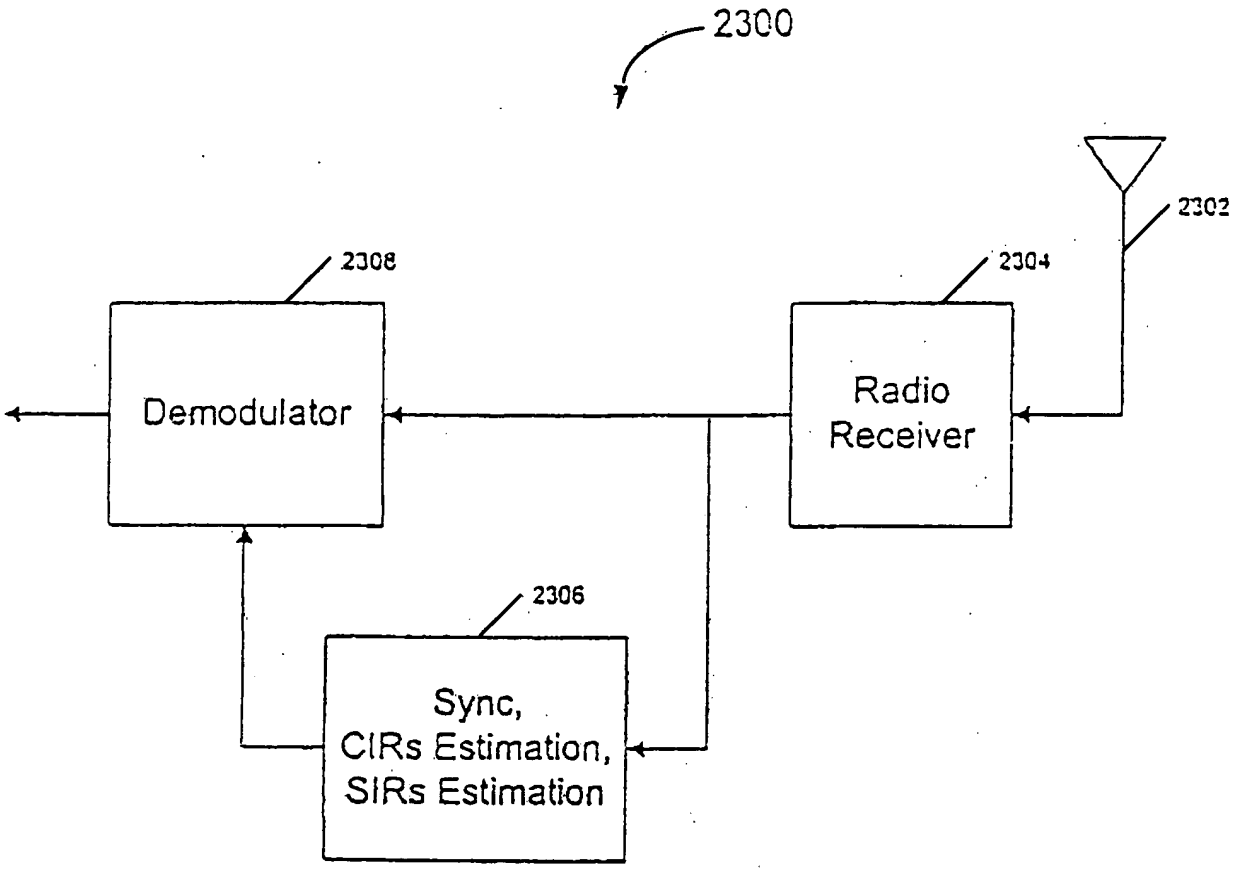
2300
2302
2304
Radio Receiver
2308
Demodulator
2306
Sync, CIRs Estimation, SIRs Estimation

Figure 23

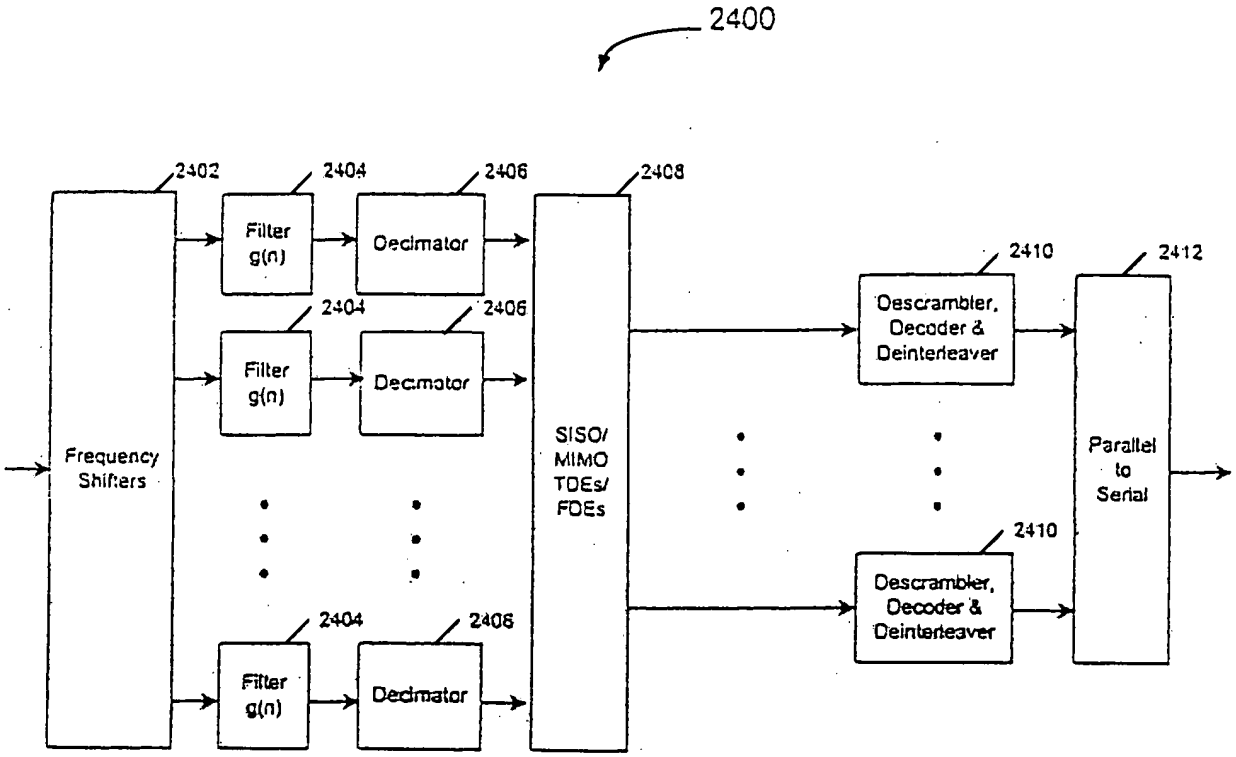
2400
2402
2404
2406
2408
2410
2412
Frequency Shifters
Filter g(n)
Decimator
SISO/ MIMO TDEs/ FDEs
Descrambler, Decoder & Deinterleaver
Parallel to Serial

Figure 24

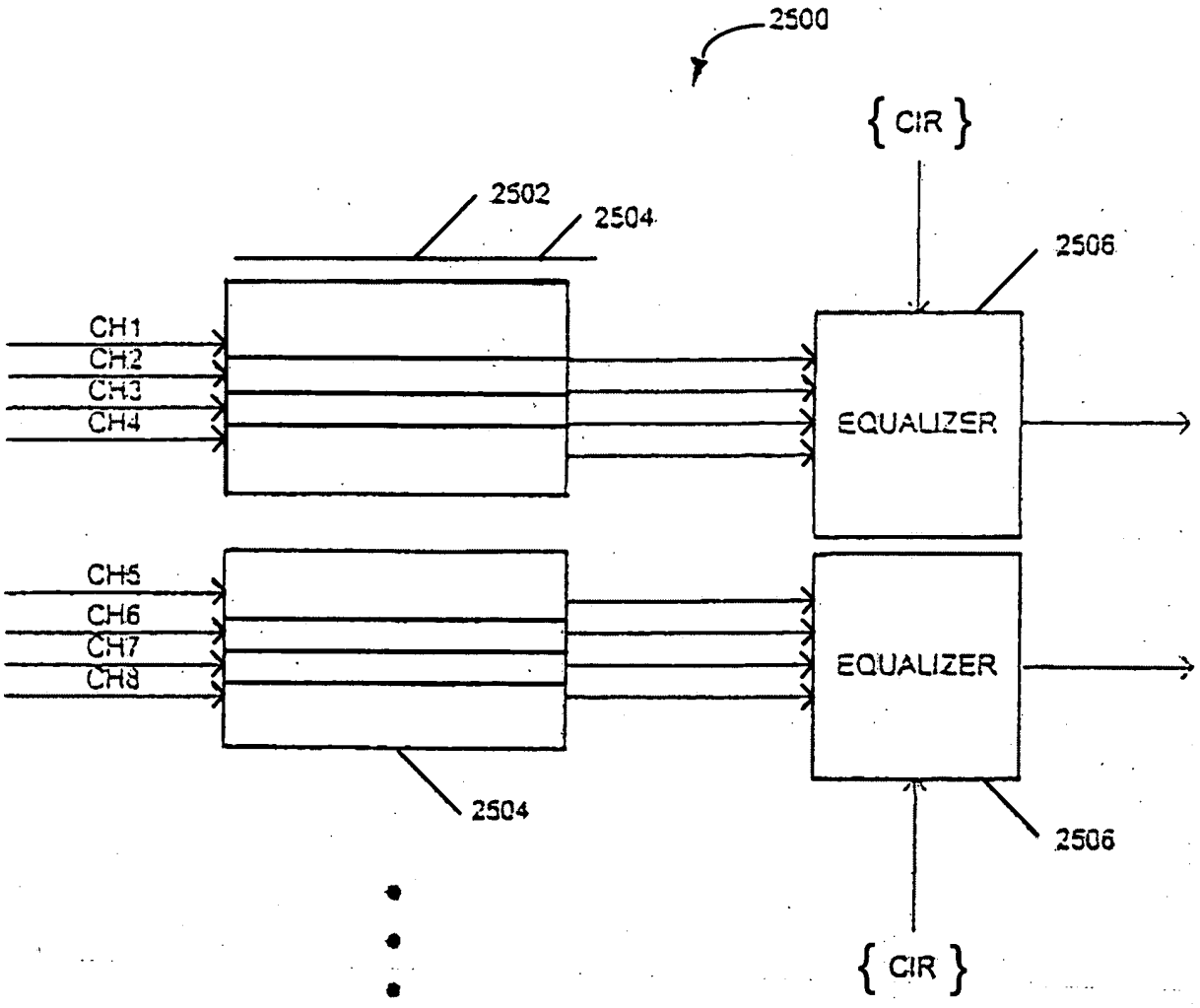
2500
{ CIR }
2502
2504
2506
CH1
CH2
CH3
CH4
EQUALIZER
CH5
CH6
CH7
CH8
EQUALIZER
2504
2506
{ CIR }

Figure 25

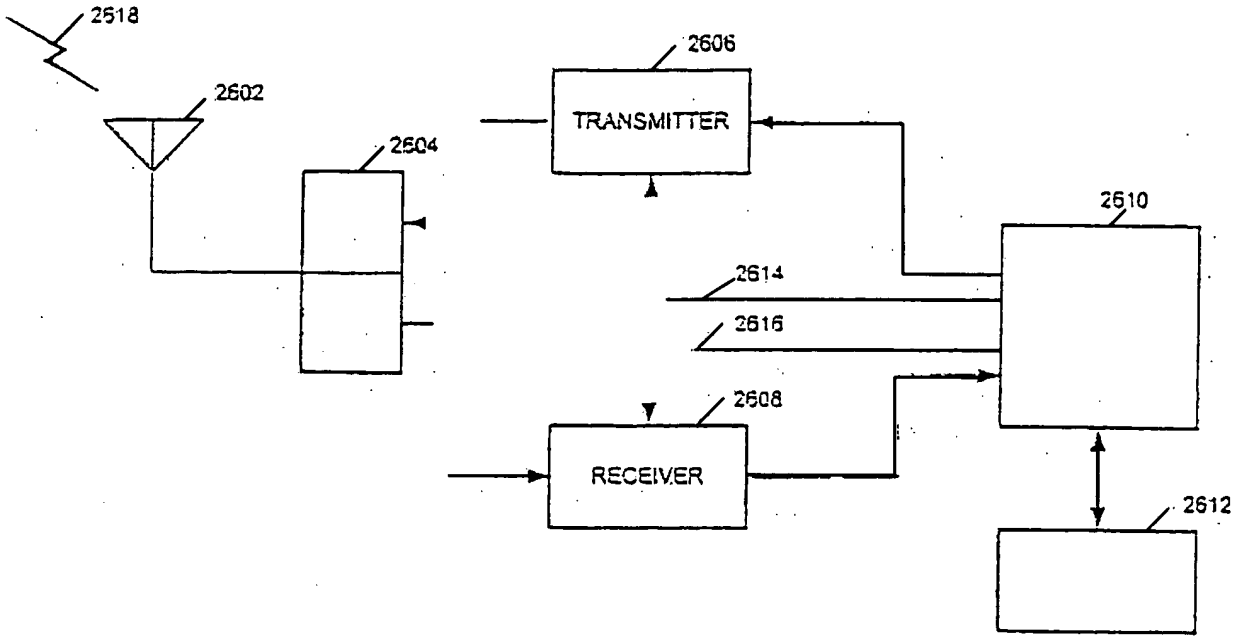
2618
2602
2604
2606
TRANSMITTER
2610
2614
2616
2608
RECEIVER
2612

Figure 26